



European Technology Summit



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Stelios heads Synopsys' Autonomous Design program and is a founder and director of the Machine Learning Center of Excellence, where he looks at applying machine learning (ML) technology to key disruptions in the design and manufacturing of integrated computational systems. He has more than 20 years of experience in chip design and EDA software, and has founded two companies in this space. Stelios holds a B.Eng. Computer Engineering from the University of Kent, UK, and an M.S. Electrical Engineering from Stanford University, California.

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